

ASSISTANT COMMISSIONER FOR PATENTS
Washington, D.C. 20231

Dear Sir:

Transmitted herewith for filing is the patent application of:

Inventor(s): **Yoshikazu KASUYA**For: **METHODS FOR MANUFACTURING SEMICONDUCTOR
DEVICES AND SEMICONDUCTOR DEVICES**JC821 U.S. PTO
09/963903
09/26/01

Enclosed are:

26 Pages of text and 13 Sheet(s) of drawings (X formal informal) + 0 extra copies
 A certified copy of 2000-292142, filed 26 September 2000, in Japan, from which priority is claimed in the subject case pursuant to Rule 55(b) and 35 USC 119. X Will follow.
 Declaration and Power of Attorney. X Will Follow.

CALCULATION OF FEES					
ITEM		NUMBER OF CLAIMS FILED MINUS BASE*	NUMBER OF CLAIMS OVER BASE x RATE SM/LG ENTITY	\$ AMOUNT	\$ FEE
A	TOTAL CLAIMS FEE	23 - 20* =	x \$9.00 or 3 x \$18.00	\$54	
B	INDEPENDENT CLAIMS FEE**	4 - 3* =	x \$40.00 or 1 x \$80.00	\$80	
C	SUBTOTAL - ADDITIONAL CLAIMS FEE (ADD FINAL COLUMN IN LINES A + B)			\$134.00	
D	BASIC FEE - SMALL ENTITY FEE = \$355.00 / LARGE ENTITY FEE = \$710			\$710.00	
E	MULTIPLE-DEPENDENT CLAIMS FEE - SMALL ENTITY FEE = \$130.00 / LARGE ENTITY FEE = \$260			\$0	
F	ASSIGNMENT RECORDING FEE			\$ 40.00	\$0
G	TOTAL FEE (ADD LINES C, D, E AND F)			\$844.00	

**LIST INDEPENDENT CLAIMS 1, 9, 14, 15

 A check in the amount of \$ 844.00 to cover the filing fee is enclosed. A copy of this letter is enclosed.

The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-0585. A duplicate copy of this sheet is enclosed.

Any additional filing fees required under 37 CFR 1.16.

Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,



Alan S. Raynes, Reg. No. 39,809

Date: September 26, 2001

KONRAD RAYNES & VICTOR LLP
315 South Beverly Drive, Suite 210
Beverly Hills, CA 90212
(310) 556-7983

Customer No. 24033

09/26/01
ASSISTANT COMMISSIONER FOR PATENTS
Washington, D.C. 20231

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09/26/01 Inventor(s): **Yoshikazu KASUYA**

For: **METHODS FOR MANUFACTURING SEMICONDUCTOR
DEVICES AND SEMICONDUCTOR DEVICES**

jc621 U.S. PTO
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of)
Yoshikazu KASUYA)
Serial No.: To Be Assigned)
Filing Date:)
For: METHODS FOR MANUFACTURING)
SEMICONDUCTOR DEVICES AND)
SEMICONDUCTOR DEVICES)

CERTIFICATE OF MAILING

Assistant Commissioner for Patents
Washington, DC 20231

Express Mail Label No.: EF 295 454 866 US
Date of Deposit: September 26, 2001

I hereby certify that a utility patent application and patent application fee transmittal; patent application including 26 pages of text (including an abstract and 23 claims), 13 sheets of drawings; a \$ 844 check for the filing fee; and a return postcard are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231.

September 26, 2001
Date of Deposit

Alan S. Raynes
Name of person mailing papers

Alan S. Raynes
Signature